



Architecture diagrams for electronics

Remote Desktop for Electronic Design Automation



Remote Desktop for Electronic Design Automation: Architecture diagrams for electronics

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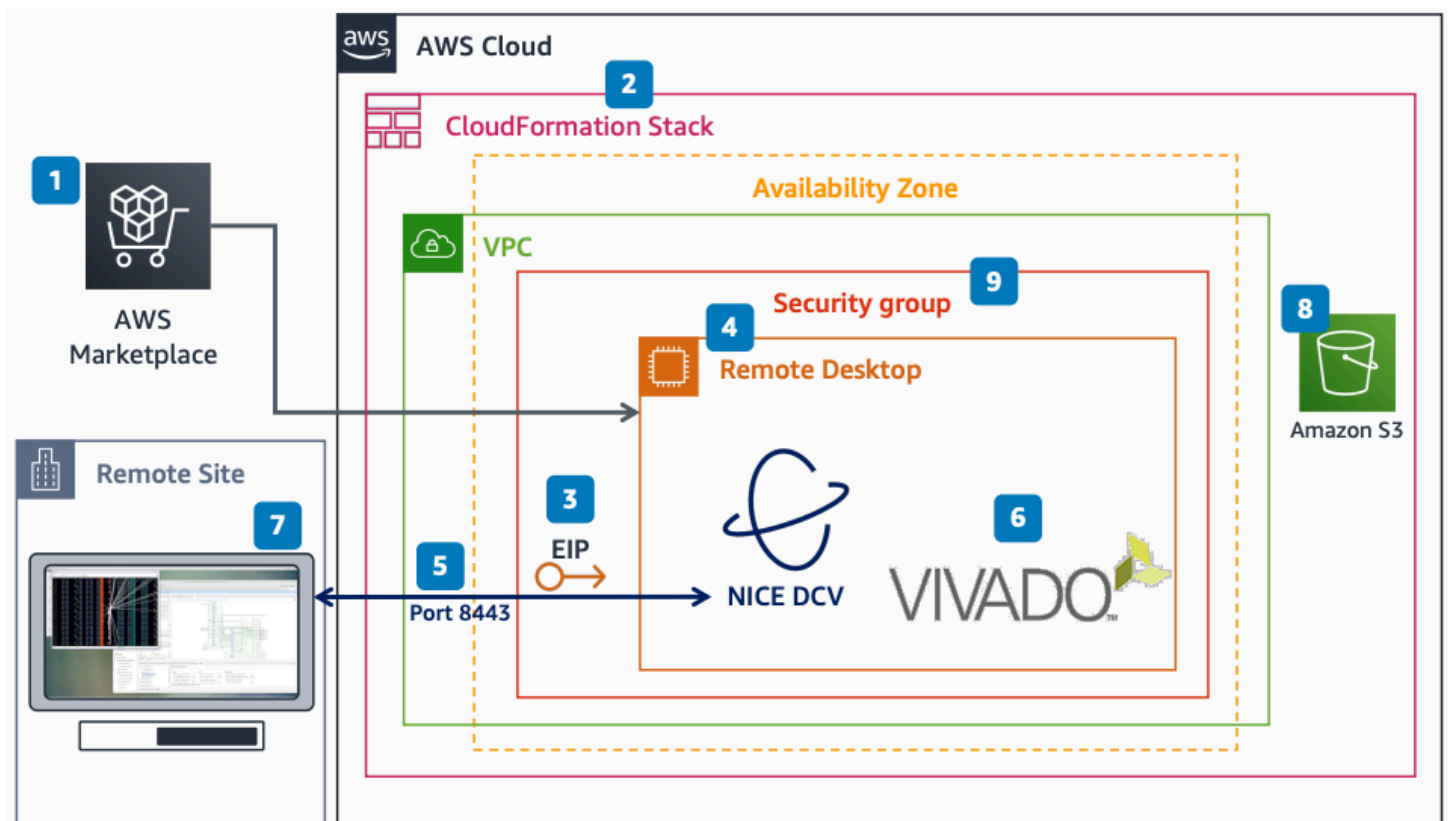
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For more information about this workshop, see [aws-remote-desktop-for-eda](#) on GitHub.

Remote desktop for Electronic Design Automation diagram



1. Subscribe to the FPGA Developer AMI in AWS Marketplace. The Xilinx Vivado Design Suite is included with this AMI.
2. Specify required parameters (Amazon VPC, subnet, Availability Zone) and launch the [AWS CloudFormation](#) stack.
3. (Optional) Create an Elastic IP address for a persistent IP.

4. Choose a remote desktop instance type that works for your tools.
5. Connect to Amazon DCV by using the Amazon DCV client or a web browser on port 8443.
6. In the FPGA Developer AMI, launch the Xilinx Vivado Design Suite by typing vivado in a terminal window.
7. The remote desktop displays on your local system.
8. (Optional) Configure [Amazon Simple Storage Service](#) bucket access to load design data.
9. (Optional) Specify additional existing security groups.

Further reading

For additional information, see the following resources:

- [AWS Architecture Icons](#)
- [AWS Architecture Center](#)
- [AWS Well-Architected](#)

Diagram history

To receive updates about this reference architecture diagram, subscribe to the RSS feed.

Change	Description	Date
Initial publication	Reference architecture diagram first published.	April 5, 2021

RSS subscription requirement

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